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D-Wave 2000Q, FPGA: Kintex-7 KC705 Schemes [5]. However, The Resource Consumption Of State-of-the-art Hardware Accelerators Are Often Out Of Reach For Edge Devices. Feb 3th, 2024 Accelerated Methods For Deep Reinforcement Learning Benchmarked Atari-2600 Domain Via The Arcade Learning Environment (ALE) (Bellemare Et Al., 2013). We Found That Highly Parallel Sampling Using Batched Inferences Can Accelerate Experiment Turn-around Time Of All Algorithms Without Hindering Training. We Further Found That Jun 1th, 2024.

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