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Classless Addressing: CIDR CIDR: Classless InterDomain Routing • Network Portion Of Address Is Of Arbitrary Length • Addresses Allocated In Contiguous Blocks - Number Of Addresses Assigned Always Power Of 2 • Address Format: Abcd/x Csci 183/183W/232: Computer Networks Forwarding And Routing Network Layer Part I 6 A.b.c.d/x Feb 1th, 2024

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### **The 8085 Microprocessor - WordPress.com**

A Microprocessor Which Has N Data Lines Is Called An N-bit Microprocessor I.e., The Width Of The Data Bus Determines The Size Of The Microprocessor. Hence, An 8-bit Microprocessor Like 8085 Can Handle 8-bits Of Data At May 4th, 2024

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ADDITION OF TWO 8 BIT NUMBERS AIM: To Perform Addition Of Two 8 Bit Numbers  
Using 8085. ALGORITHM: 1) Start The Program By Lo Feb 14th, 2024

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The Microprocessor Is The Master, Which Controls All The Activities Of The System.  
To Perform A Specific Job Or Task, The Microprocessor Has To Execute A Program  
Stored In Memory. The Program Consists Of A Set Of Instructions. It Issues Address  
And Control Sign Feb 8th, 2024

## **HISTORY OF MICROPROCESSOR AND 8085 INTRODUCTION**

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