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Oct 07, 2012 · The ADC080X Analog-to-Digital Converter O Generating The Clock Signal: ADC0804 Requires A Clock Source To Operate. O It Can Be An External Clock Applied To CLK IN Pin Or Can Be Generated With An RC Circuit. O Permissible Range Of Clock Frequencies Is 100KHz-1460 KHz. O Jan 17th, 2024

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.the Module Terminals. The Digital Output Code Of The Binary Version Is Natural Binary For A Unipolar Input, But Is Selected By The User To Be Either Offset Binary Or Two's Complement With A Bipolar Input. The ADC-QM Is Availa Jan 16th, 2024

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11 Procedure Coherent Demodulation 1. Generate An AM Signal As Done In Part 'a' Of Previous Experiment.
2. Using The Mixer Module On The Trainer, Demodulate The Signal With Carrier Wave; Note That The Carrier Should Be The Same As Used In The Modulation Process. 3. Mar 7th, 2024

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