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### **Figure 1. Arria V GT FPGA Development Board With Two ...**

Development Kit. Development Kit Contents The Arria V GT FPGA Development Kit Features The Following: Arria V GT FPGA Development Board (see Figure 1) Unit 1: Arria V GT FPGA: 5AGTFD7K3F40I3N Memory 1,152 MB X72 DDR3 SDRAM 4.5 MB (1 Mb X 36) QDR II+ SRAM 1 Gb Sync Flash (x16) Communication Ports ® Arria V GT FPGA Development Kit Page 2 Of 6 Feb 12th, 2024

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101 Innovation Drive, San Jose, CA 95134. Figure 1-2: Arria 10 GX Block Diagram FMC Altera LPC+ 9 8 X2 6 8 X8 XCVRs PCI Express 128MB FLASH X32 CPLD (FPGA) MAX V Onfig Oscillators 50M, 125M, Programmable X1 (SE) Buttons Switches LEDs X4 X8 X16 5 Micro-USB 2.0 JTAG Chain SMA CLK IN Jan 7th, 2024

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September 2015 Altera Corporation Arria V SoC Development Kit User Guide 1. About This Kit The Altera® Arria® V System On A Chip (SoC) Development Kit Is A Complete Design Environment That Includes Both The Hardware And Software You Need To Develop Arria V SoC Designs. Kit May 12th, 2024

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Kit Project Files, Refer To The “Software Installation” Chapter Of The Arria V SoC Development Kit User Guide. 2. Quartus II We May 10th, 2024

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