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- Introduction
- What Is Analog Design?
- Skillset For

Analog IC Circuit Design • Trends In Analog IC Design • Notation, Terminology And Symbols • Summary CMOS Analog Circuit Design, 3rd Edition Reference Pages 1-16 . Lecture 01 – Introduction (7/6/15) Page 01-2  
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## **ECE 584 Analog CMOS Integrated Circuit Design Spring 2021**

Analog Integrated Circuit Design John Wiley & Sons  
Tony Carusone, David A. Johns, Kenneth W. Martin  
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“cmos Analog Circuit Design,” Oxford University Press, New York, 2004. Has Been Cited By The Following

Article: Holberg, Cmos Analog Circuit Design, Holt, .

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DESCRIPTION The Subject Of This Course Is The Analysis And Design Of CMOS Analog Integrated Circuits At The Transistor Level, With An Emphasis On Intuitive Design Methods, Quantitative Performance Measures And Practical Circuit Limitations. Apr 1th, 2024

### **EECE488: Analog CMOS Integrated Circuit Design**

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SM 3 EECE 488 - Set 1: Introduction And Background References • Main Reference: Lecture Notes •

Recommended Textbook: Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw- Hill, 2001 • Some Other Useful References: Jan 8th, 2024

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EECE488 Analog CMOS Integrated Circuit Design

Assignment 4 Due: Tuesday November 26th, 2013 At

8:00am 1. Consider The Following Circuit. (This Circuit

Is Sometimes Referred To As A Self-biased Current Source).  $R_{V_{DD}} = 0$ ,  $M_0$ ,  $M_2$ ,  $M_1$ ,  $M_3$ ,  $GND_4$ ,  $GND_1$  Out Assume All Transistors Are Operating In Saturation Region And  $V_{DD} = 3V$ ,  $V_{TH(NMOS)} = 0$  ... Apr 14th, 2024

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EECE488: Analog CMOS Integrated Circuit Design Set 2: Background Shahriar Mirabbasi Department Of Electrical And Computer Engineering University Of British Columbia Shahriar@ece.ubc.ca Technical Contributions Of Pedram Lajevardi In Revising The Slides Is Greatly Acknowledged. SM 2 EECE 4 Jan 8th, 2024

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