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1.6.5 Advantages Of Flowchart 1.6.6 Limitations Of Flowchart 1.7 Pseudocode Example 1.8 Divide And Conquer Strategy 1.8.1 Merge Sort 1.9 Writing Algorithms And Flow Charts For Simple Exercises 1.10 Let Us Sum Up 1.11 Lesson And Activity 1.12 Keywords 1.13 Questions For Discussion Apr 10th, 2024

BCA - PGDCA SEM-2 (NEW Course) BCA/DCA 201 Advanced ...

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BCA Revised Syllabus (w.e.f. 2019-20 And Onwards) Subject Code Subject Title
Teaching Scheme Hrs/week Examination Exam. Duration (Hrs) Marks Theory
Practical Theory/ Practical IA Total Apr 2th, 2024

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Universal Gate (NAND) Equipment The Equipment You Require Is As Follows: IC
7404, IC 7400, IC 7483 A Half Adder Is A Logical Circuit That Performs An Addition

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Question No.1 To 10 - All Are Multiple Choices (2 Questions From Each Unit). Part - B (5 X 7 =35) Choosing Either (a) Or (b) Pattern (One Question From Each Unit). ... UG Programme - BCA (2018 - 2021) Part Title Sem I Sem II Sem III Sem IV Sem V Sem VI ... Programming In Java Lab 5 4 Feb 8th, 2024

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University. 3. Elizabeth A. Dickson, Computer Program Design, Tata McGraw Hill. ...
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