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High-level Description Of Verilog Verilog For Computer DesignHigh-level Description Of Verilog \bullet Verilog Syntax \bullet Primitives \bullet Number Representation \bullet Modules And Instances \bullet Wire And Reg Variables \bullet Operators \bullet Miscellaneous \bullet Parameters, Pre-processor, Case State 1th, 2024VERILOG HDL - ANUCompiler \rightarrow Assembly Code \rightarrow Binary Machine Code Synthesis

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