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(1) Multiplexers And De-multiplexers Objectives To Analyze The Logic Operation Of  
A 4 Lines To 1 Line Multiplexer (data Selector) Circuit And A 1 Line To 4 Line De-  
multiplexer Circuit. Instruments DL 3155M18 Module (unit #3), P Apr 2th,  
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Contraction Of AND-NOT. The Output Is High When Both Inputs Are Low And Any  
One Of The Input Is Low .The Output Is Low Level When Both Inputs Are High. NOR  
GATE: SL No. COMPONENT SPECIFICATION QTY 1. AND GATE IC 7408 1 2. OR GATE  
IC 7432 1 3. NOT GATE IC 7404 1 4. NAND GATE 2 I/P IC 7400 1 5. NOR GATE IC

7402 1 Mar 5th, 2024.

DIGITAL ELECTRONICS LAB (EE-224-F) IV SEMESTER(7400, 7402, 7404, 7408, 7432, 7486) BRIEF THEORY: AND Gate: The AND Operation Is Defined As The Output As One If And Only If All The Inputs Are One. 7408 Is The Two Input AND Gate IC.A&B Are The Input Terminals &Y Is The Output Terminal.  $Y = A.B$  May 5th, 2024E85: Digital Electronics And Computer Engineering Lab 2 ...The Lab Tutorial Will Walk You Through A Full Adder And Then You Will Design An Instruction Decoder Circuit. 1. Tutorial: Altera FPGA Tools All Of The FPGA Labs In E85 Will Be Using The Altera/Intel Quartus Prime FPGA Software (Version 18 Was Current As Of This Writing) And The Altera DE0-CV Apr 3th, 2024Digital Electronics LabLab Manual: Digital Electronics Lab (EE-224-F) DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING Page 2 STUDENTS GUIDELINES There Is 1Hr 40 Minutes Allocated To A Laboratory Session In Digital Electronics. It Is A Necess Jun 5th, 2024.

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