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3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Apr 2th, 2024 IEEE Signal Processing Society Signal Processing Magazine ...2015 Zhi-Quan Luo, Wing-Kin Ma, Anthony Man -Cho So, Yinyu Ye And Shuzhong Zhang , For The Paper Entitled, Semidefinite Relaxation Of Quadratic Optimization Problems, IEEE Si May 1th, 2024.
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