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John .p.Hayes,"Computer Architecture And Organization", Tata McGraw Hill, 1996. 2. V.C.Hamatcher, et Al "Computer Organization", Tata Mcgraw Hill, 1996 . Page 5 Of 47 DMC1912 PROBLEM SOLVING AND PROGR Mar 3th, 2024

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IEEE Signal Processing Society Signal Processing Magazine ...

2015 Zhi-Quan Luo, Wing-Kin Ma, Anthony Man -Cho So, Yinyu Ye And Shuzhong Zhang, For The Paper Entitled, Semidefinite Relaxation Of Quadratic Optimization

Problems, IEEE Si Jul 1th, 2024

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Programmable Logic Devices – Functions Of Combinational Logic – Flip Flops And Related Devices – Counters – Shift Registers – Memory And Storage – Introduction To Microprocessors – Architecture Of 8085 / 8086 – Assemble Language Programming – Peripherals – Integrated Mar 3th, 2024

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1. Monson H. Hayes, "Statistical Digital Signal Processing And Modeling", John Wiley And Sons, Inc, Singapore, 2002. 2. Lawrence Rabiner And Biing-Hwang Juang,

"Fundamentals Of Speech Recognition", Pearson Education, 2003. 3. Dimitris G. Manolakis And Vinay K .Ingle, "Appli Jun 2th, 2024

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History Of Chennai: Http://en.wikipedia.org/wiki/Chennai

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