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Example4: 3-bit Up/Down Counter The 3-bit Up/Down
...The 3-bit Up/Down Counter Was Earlier Implemented
Using J-K Flip-flops. A D Flip-flop Based 3-bit Up/Down
Counter Is Implemented By Mapping The Present State
And Next State Information In D Input Table. Table
36.1. The Karnaugh Maps And The Simplified Boolean
Expressions Derived From The D Input Table, Table
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