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Overview Of Power Consumption • The Average Power Consumption Can Be Expressed As $1 \text{ Avg } C \text{ Load } V_{DD} C \text{ Load } V_{DD} F_{CLK} T P 2$ • The Node Transition Rate Can Be Slower Than The Clock Rate. To Better Represent This Behav Mar 1th, 2024

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The Final Output From The Design Process Is The Full Chip Layout, Mostly In The GDSII (gds2) Format To Produce A Functionally Correct Design That Meets All The Specifications And Constraints, Requires A Combination Of Different Tools In The

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