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Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory • T Cache =
1 Cycle, T MM = 100 Cycles • What Is The AMAT Of The Program From Example 1?
 $AMAT = T_{Cache} + MR_{Cache} (t_{MM}) = [1 + 0.375(100)] \text{ Cycles} = 38.5 \text{ Cycles}$
Memory Performance Example 2 Apr 3th, 20247-1 Chapter 7- Memory System
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A Matter Of Time And Sophiatown Would Be Reborn" (Mattera, 2009, P. 152). Mattera Would Probably Be The First To Admit That Memory Does Not Offer Complete Salvation. In A Seemingly Discordant And Hanging Last Sentence Of The Book - A Sente Apr 4th, 2024
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Hierarchy And Virtual Memory Storage Device Speed Vs. Size Facts: •CPU Needs Sub-nanosecond Access To Data To Run Instructions At Full Speed •Fast storage (sub-nanosecond) Is Small (100-1000 Bytes) •Big Storage (gigabytes) Is Slow (15 Nanoseconds) •Huge storage (terabytes) Is Glacially slow (milliseconds) Goal: •Need Many Gigabytes Of Memory Mar 9th, 2024.

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