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Figure 1. Arria V GT FPGA Development Board With Two ...Development Kit. Development Kit Contents The Arria V GT FPGA Development Kit Features The Following: Arria V GT FPGA Development Board (see Figure 1) Unit 1: Arria V GT FPGA: 5AGTFD7K3F40I3N Memory 1,152 MB X72 DDR3 SDRAM 4.5 MB (1 Mb X 36) QDR II+ SRAM 1 Gb Sync Flash (x16) Communication Ports ® Arria V GT FPGA Development Kit Page 2 Of 6 Jan 2th, 2024Arria 10 FPGA Development Kit User Guide101 Innovation Drive, San Jose, CA 95134. Figure 1-2: Arria 10 GX Block Diagram FMC Altera LPC+ 9 8 X2 6 8 X8 XCVRs PCI Express 128MB FLASH X32 CPLD (FPGA) MAX V Onfig Oscillators 50M, 125M, Programmable X1 (SE) Buttons Switches LEDs X4 X8 X16 5 Micro-USB 2.0 JTAG Chain SMA CLK IN Jan 4th, 2024Device Datasheet And Addendum For The Arria II Device ...101 Innovation Drive San Jose, CA 95134 Wwww.altera.com AIIIGX5V3-4.0 Volume 3: Device Datasheet And Addendum Arria II Device Handbook Docume Apr 4th, 2024.

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