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- 1.Request Occurs On Or Before T2.
- 2.Current Cycle Is Not The Low Byte Of A Word (on An Odd Address).
- 3.Current Cycle Is Not The First Acknowledge Of An Interrupt Acknowledge Sequence.
- 4.A Locked Instruction Is Not Currently Executing.

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Requires Single Phase Clock With 33% Duty Cycle To

Provide Internal Timing. • 8086 Is Designed To Operate

In Two Modes, Minimum And Maximum. • I Mar 2th,

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...8086 Microprocessor Is Interfaced With Memory And

Peripherals And How An 8086 Based Microcomputer

System Works. 3.2 Learning Objectives To Study About

The Operating Modes Of 8086 To Study About The

Components Of An 8086 Based Microcomputer System

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Is Available As 40-pin Dual-Inline-Package (DIP). 5. It Is
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