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Caches And Hash Trees For Efficient Memory Integrity ...Blaise Gassend, G. Edward

Suh, Dwaine Clarke, Marten Van Dijk† And Srinivas Devadas Massachusetts Institute Of Technology Laboratory For Computer Science Cambridge, MA 02139, USA {gassend,suh,declarke,marten,devadas}@mit.edu Abstract We Study The Hardware Cost Of Implementing Hash-tree Jun 1th, 2024Caches And Merkle Trees For Efficient Memory AuthenticationBlaise Gassend, G. Edward Suh, Dwaine Clarke, Marten Van Dijk, Srinivas Devadas MIT Laboratory For Computer Science July 20, 2002 Abstract We Describe A Hardware Scheme To Authenticate All Or A Part Of Untrusted External Memory Using Trusted On-chip Storage. Our Scheme Uses Merkle May 1th, 2024Lecture 11: Caches (Basics) - Uoc.grIMac G5 1.6 GHz 107 Cyc, 1600x (mem: 73x) 12 Ms Apple Il Goal: Illusion Of Large, Fast, Cheap Memory Let Programs Address A Memory Space That Scales To The Disk Size, At A Speed That Is Usually Nearly As Fast As Register Access IMac G5 Feb 2th, 2024.
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