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(Dr. Ramakant Srivastava) Passive Sign Convention (PSC)

Two Elements May Neither Be In Series Nor In Parallel. Example 1.8 In The Circuit Shown In Figure 1.11, The Voltage Source Is In Series With R 1 And The Current Source Is In Parallel With R 4. None Of The Three Resistors Is Either In Series Or In Parallel With Any Other Resistor. R 1 R2 R3 Vs I R4 S Figure 1.11: May 7th, 2024

Common Source Amplifier MOSFET Amplifier Distortion

ECE315 / ECE515 MOSFET Amplifier Distortion (contd.) • Note For This Example, The DC Output Voltage Is The DC Drain Voltage, And That Its Value Is: VV OD V • Thus, The Total Output Voltage Is: 10.0 5.0 Cos O D O I V T V V T V ĐW It Is Very Important That You Realize There Feb 4th, 2024

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ESE319 Introduction To Microelectronics 2008 Kenneth R. Laker (based On P. V. Lopresti 2006) Updated 01Oct08 KRL 1 Common B Feb 3th, 2024

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