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Chapter 8
 Memory Hierarchy And Cache Memory • Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory • $T_{Cache} = 1 \text{ Cycle}$, $T_{MM} = 100 \text{ Cycles}$ • What Is The AMAT Of The Program From Example 1?

AMAT = T Cache + MR Cache (t MM) = [1 + 0.375(100)] Cycles = 38.5 Cycles Memory Performance Example 2 Jan 1th, 2024Memory And Super-Memory -- I'll Never Forget What's His ...Memory Course Can Be Ordered In-dependently Of The Curriculum For \$65 From American Management Association, 135 West 50th Street, New York, NY 10020.1-A Huge Amount Of Information Is Stored In Our Memory. It Is Sometimes Difficult To Retrieve The Precise Informa-tion That We Want At A Particular Apr 2th, 2024.

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