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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Feb 3th, 2024

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October 2016 WP Leader: Tomas Kulhanek STFC Jun 1th, 2024

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A REVIEWS Probing Early Heart Development To Instruct Stem Cell Differentiation Strategies Damelys Calderon, 1,2,3†Evan Bardot, And Nicole Dubois1,2,3* 1Department Of Developmental And Regenerative Biology, Icahn School Of Medicine At Mount Sinai, New York, New York 2Mindich Child Health And Development Institute, Ic Apr 1th, 2024

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Jun 2th, 2024

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