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Inductor Design 1 - Purdue University

A Design Example • Scenario. A Power Electronics Converter Design Requires A Filter Inductor. • Requirements ¾At Maximum Load, The Average Inductor Current Will Be 3.0 A. ¾Current Ripple Less Than 0.2 A. ¾The Incremental Inductance Required Is 5 MH. ¾The Dc Resistance Of The Induc Apr 16th, 2024

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