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...Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Apr 11th, 2024NAJJAR, Samer. B.Sc. (Eng.) M.Sc. (Eng.) PhD (Eng.) (Dr.)NAJJAR, Samer. B.Sc. (Eng.) M.Sc. (Eng.) PhD (Eng.) (Dr.) Personal Details Date Of Birth 24-01-1959 (Male/Married) Nationality Palestinian, British Address PO Box 167 Nablus Telephone Work: +970 9 2671026 Ext 1520 Home: +970 9 2341124 Jan 4th, 2024.

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