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Chapter 4 Low-Power VLSI Design Power VLSI Design

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Introduction To VLSI

-Output Pins In Combinational Cells Define: Rise_delay, Fall_delay, Rise_transition, And Fall_transition. -Output Pins In Sequential Cells Define: Rise_constraint, Fall_constraint (Setup And Hold) Hendren, Berry, Fall 2012 . Title: Introduction To VLSI Author: Joseph A. Elias Jan 2th, 2024

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